Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S71	40	"buffered nor gate"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/27 11:28
S50	28	"cmos nand".ti.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/26 07:50
S51	16	"cmos nor".ti.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/26 08:32
S88	13	"column select" same input near3 data near3 buffer same clock same enabl$3 same (pulse signal)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/28 07:25
S77	1	"half latch" same (pmos pfet) same clock same writ$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/27 12:19
S78	10	"half latch" same (pmos pfet) same writ$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/27 14:38
S37	33	"memory controller" same turn$3 adj "off" near5 clock	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/14 08:14
S109	69	"sram macro" and port	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/29 09:23
S52	766	"synchronous sram"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/26 08:32
S53	464	"synchronous sram" and delay$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/26 08:33

S54	174	"synchronous sram" and delay$3 and "write enable"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/26 08:34
S55	4	"synchronous sram" and delay$3 and "write enable" near3 latch	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/26 09:15
S35	57	"synchronous SRAM".ti.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/13 11:04
S48	399	"write enable" near3 latch	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/25 10:04
S49	5	"write enable" near3 latch near3 driver	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/25 10:04
S72	759	"write latch"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/27 11:28
S73	76	"write latch" same inverter	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/27 11:31
S90	1458	"write pulse" same clock	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/28 08:44
S91	36	"write pulse" same clock and sdram	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/28 09:13
S61	7	("4768168" | "5267197" | "5404327" | "5907508" | "6198679" | "6252814" | "6366507").PN.	US-PGPUB; USPAT; USOCR	OR	ON	2005/07/26 11:10
S10 7	5	("4996671" | "5777935" | "5946264"). PN. OR ("6130846").URPN.	US-PGPUB; USPAT; USOCR	OR	ON	2005/07/29 07:22

S106	31	("4996672" | "5124669" | "5446867" | "5566108" | "5619170" | "5621698" | "5708622" | "5717652" | "5729500" | "5740115" | "5754838").PN. OR ("5920510").URPN.	US-PGPUB; USPAT; USOCR	OR	ON	2005/07/28 14:44
S99	2	("5227671" | "5440514").PN.	US-PGPUB; USPAT; USOCR	OR	ON	2005/07/28 12:28
S96	6	("6498514" | "6686775" | "6690204" | "6690205" | "6717442" | "6833735"). PN.	US-PGPUB; USPAT; USOCR	OR	ON	2005/07/28 09:53
S39	6	(("5546355") or ("6366507") or ("5920510")).PN.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/07/25 09:34
S87	4	(("5896341") or ("5402389")).PN.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/07/28 07:23
S46	10	(2-port two-port "two port" multiport "2 port" multi-port "multi port") near5 sram near5 synchronous	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/25 10:03
S7	97	(326/28).CCLS.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/15 12:10
S8	997	(326/93).CCLS.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/15 12:11
S9	162	(326/96).CCLS.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/15 12:12
S11	1324	(327/172).CCLS.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/15 12:17
S12	292	(327/174).CCLS.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/15 12:17

S13	671	(327/175).CCLS.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/15 12:17
S10	1275	(327/291).CCLS.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/15 12:17
S47	2733	(365/189.05).CCLS.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/07/25 10:04
S21	1501	(365/194).CCLS.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/07/13 07:15
S20	3571	(365/230.06).CCLS.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/07/13 07:15
S3	3076	(365/233).CCLS.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/15 11:31
S4	637	(365/233.5).CCLS.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/06/15 11:31
S16	134	(S11 S12 S13) and nand same inverter same delay	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/15 12:30
S62	95	(sram "static ram") near3 timing same write near3 operation	US-PGPUB; USPAT; USOCR	OR	ON	2005/07/26 13:08
S57	165	(stably stable stability) same writ$3 near3 operation same sram	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/26 09:33

S58	33	(stably stable stability) same writ$3 near3 operation same sram same (delay$3 tim$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/26 09:58
S68	94	(synchronous adj ("static random" sram)).ti.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/26 14:36
S67	117	(synchronous adj (static sram)).ti.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/26 14:06
S66	283	(synchronous and (static sram)).ti.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/26 14:06
S36	7	(synchronous and sram).ab. and "low power".ti.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/14 07:42
S30	48	decod$3 near5 clock near3 delay$3 and sram	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/13 07:29
S29	314	decod$3 same clock near3 delay$3 and sram	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/13 07:29
S44	1310	decoder.ti. and (dram sdram sram eeprom)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/25 09:48
S45	61	decoder.ti. and (dram sdram sram eeprom) and latch and "write enable"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/25 09:56
S82	36	delay$3 same block near3 select same column near3 select	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/27 15:02

L2	102	dynamic same NAND same latch same clock	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/08/01 08:26
S11 5	36	embedded with macro with sram	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/29 11:41
S74	3	hur.xa. and horiguchi.in.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/27 11:55
S75	2211	latch same (pmos pfet) same inverter	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/27 11:56
S76	389	latch same (pmos pfet) same inverter same (decod$3 writ$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/27 12:18
S40	328	latch same driver same "write enable"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/25 09:39
S41	4	latch same driver same "write enable" same (pfet pmos)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/25 09:45
S97	54	nand same half near3 latch same inverter	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/28 11:12
S92	268	nand same half same latch same inverter	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/28 10:17
S95	27	pulse near3 generat$3 same clock same half near3 latch	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/28 09:34

S93	773	pulse same generat$3 same clock same half same latch	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/28 09:34
S10 2	262	S101 and (column bitline "bit line") near3 select$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/28 12:37
S10 3	424	S101 and write near3 (signal pulse)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/28 12:38
S10 4	424	S101 and write near3 (signal pulse)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/28 12:44
S10 5	100	S101 and write near3 (signal pulse) same delay$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/28 12:44
S24	122	S20 and S21	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/13 07:16
S25	77	S24 and clock	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/13 07:16
S26	29	S24 and clock and sram	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/13 07:17
S27	42	S24 and clock near3 delay$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/13 07:17
S28	11	S24 and clock near3 delay$3 and sram	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/13 07:29

S86	446	S85 and @pn>"6000000"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/28 07:07
S64	315	self near3 tim$3 near3 writ$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/26 13:40
S65	66	self near3 tim$3 near3 writ$3 same clock	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/26 14:05
S112	3	sram near3 macro same (multi-port two-port)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/29 10:13
S111	1	sram near3 macro same (two-port)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/29 09:24
S31	196	sram near3 stability	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/13 08:28
S32	45	sram near3 stability same writ$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/13 09:48
S110	80	sram same macro same port	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/29 09:24
S85	1050	synchronous adj (sram "static random")	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/28 12:32
S101	1050	synchronous adj (sram "static random")	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/28 12:32

S6	2133	synchronous near3 sram	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/13 07:15
S14	182	synchronous near3 sram and (S3 S4)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/15 13:30
S17	11	synchronous near3 sram and self adj tim$3 adj clock	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/15 13:40
S18	154	synchronous near3 sram same (write writing) near3 (mode operation cycle)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/15 13:40
S15	76	synchronous near3 sram same cycle same write same clock	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/15 12:30
S59	210	writ$3 near3 operation same sram same turn$3 near3 off	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/26 09:59
S60	22	writ$3 near3 operation same sram same turn$3 near3 off near5 (word adj line wordline)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/26 09:59
S80	1737	write near3 select$3 near3 (signal pulse) same (column bit data) near3 select$3 near3 (signal pulse)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/27 14:39
S81	62	write near3 select$3 near3 (signal pulse) same (column bit data) near3 select$3 near3 (signal pulse) same sram	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/27 15:01
S100	3	write with wider with (column "bit line" bitline) near3 select$3	US-PGPUB; USPAT; USOCR	OR	ON	2005/07/28 12:29